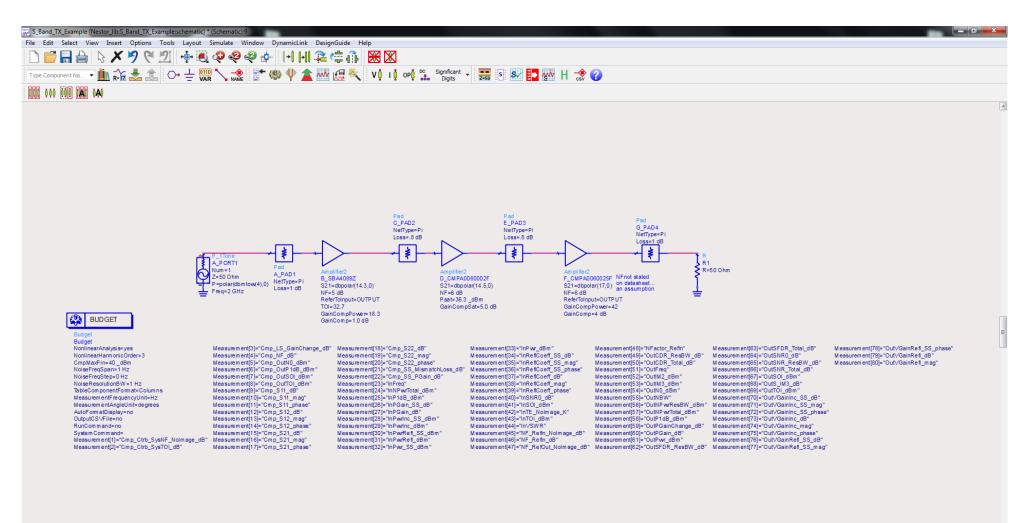
Two Examples Of Cascaded RF Analysis Using The RF Budget Simulator In The Advanced Design System (ADS) Simulator.

The RF Budget Simulator Is A Legacy Tool From Keysight Which Is No Longer Officially Supported, But It Still Yields Valuable Insight To Linear

And Non-Linear Cascaded RF Signal Chains.

Other Advanced Design System Examples Can Be Found At: https://bbt-line.com/ads-examples/



WV S_Band_TX_Example* [page 1]:15													
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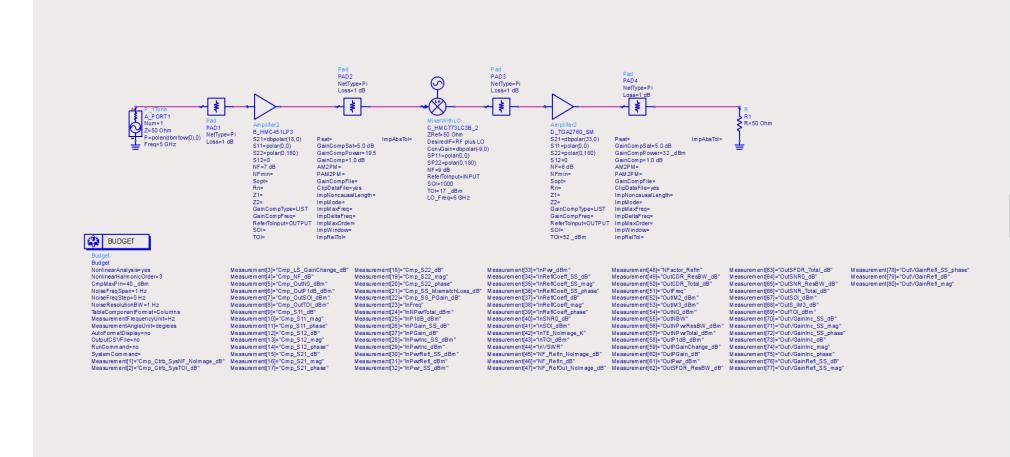
page 1

ge Parameters (NC	OT cascaded)	Gain [dB]	NF [dB]	Output P1dB Compression [dBm]	Output TOI [dBm]
Cmp_Index	Cmp_RefDes	Cmp_S21_dB	Cmp_NF_dB	Cmp_OutP1dB_dBm	Cmp_OutTOI_dBm
0 1 2 3 4 5 6	A PAD1 B_SBA4089Z C PAD2 D_CMPA0060002F E_PAD3 F_CMPA0060025F G_PAD4	-1.0 14.3 -0.8 14.5 -0.8 17.0 17.0 -1.0	1.0 5.0 0.8 6.0 0.8 6.0 1.0	1000.0 18.3 1000.0 32.3 1000.0 39.7 1000.0	1000.0 32.7 1000.0 42.7 1000.0 50.3 1000.0

Stage Parameters, S			dB] of Stag	ge[dḃ] in	, moving forward chain [dB]	NF, moving backwarc in chain [dB]		
Cmp_Index	Cmp_RefDes	InPGain_S	SS_dB OutVGai	inInc_SS_dB NF_	RefIn_Nolmage_dB	RefOut_Nolmage_dB	InTOI_dBm	
0 A P/ 1 B_SBA0 2 C P 3 D_CMPA00600 4 E P/ 5 F CMPA00600		PAD2 0002F PAD3	0.0 -1.0 13.3 12.5 27.0 26.2 43.2	-1.0 13.3 12.5 27.0 26.2 43.2 42.2	1.0 6.0 6.2 6.2 6.2 6.2 6.2	62 5.2 6.9 6.1 6.8 6.0 1.0		<u>5.3</u> 5.3 9.9 9.1 4.1 3.3 0.0
						\checkmark	/	
		total ca gain	scaded small-signa	I	total ca	scaded NF	total Input TOI [dB	m]
Single Tone Large Si	gnal Analysis	Power at Input To Stage [dBm]	Power at Output of Stage [dBm]	Freq at Input To Stage [GHz]	Freq at Outpu Of Stage [GH:		Gain at Output	Gain Deviation From Small Signal at Output of Stage [dB]
Cmp_Index	Cmp_RefDes	In Pwr_dBm	OutPwr_dBm	InFreq/1e9	OutFreq/1e9	InPGain_dB	OutPGain_dB	tPGainChange_dB
0 1 2 3 4 5 6	A_PAD1 B_SBA4089Z C_PAD2 D_CMPA0060002F E_PAD3 F_CMPA0060025F G_PAD4	4.0 3.0 16.8 16.0 30.0 29.2 42.2	3.0 16.8 16.0 30.0 29.2 42.2 41.2	2 0 2 0 2 0 2 0 2 0 2 0 2 0 2 0	2222	10 0.0 10 -1.0 10 12.8 10 12.0 10 26.0 10 25.2 10 38.2	-1.0 12.8 12.0 26.0 25.2 38.2 37.2	0.0 -0.5 -1.0 -1.0 -5.0 -5.0
Noise Power, SNR	, SFDR	Noise Power, 1 Hz BW, Input of Stage	Noise Power, 1 Hz BW, Output of Stage	SNR, Input of Stage, (difference between InPwr_dBm and InNPwrTotal_dBm)	SNR, Output of Stage, (difference betwee OutPwr_dBm and OutNPwrTotal_dBr	SEDR	Dynamic Range (difference between P1dB compression point and noisef floor), Output of Stage	P1dB compression, Output of stage
Cmp_Index	Cmp_RefDes	InNPwrTotal_dBm	OutNPwrTotal_dBm	InSNR0_dB	OutSNR0_dB	OutSFDR_Total_dB	OutCDR_Total_dB	OutP1dB_dBm
0 1 2 3 4 5 6	A_PAD1 B_SBA40892 C_PAD2 D_CMPA0060002F E_PAD3 F_CMPA0060025F G_PAD4	-173.9 -173.9 -154.6 -155.4 -140.8 -141.6 -124.5	-173.9 -154.6 -155.4 -140.8 -141.6 -124.5 -125.5	177.9 176.9 171.5 171.5 170.7 170.7 170.7	171 171 170 170 170 166	.5 124.9 .5 124.9 .7 121.3 .7 121.3 .8 116.1	1000.0 172.9 172.9 170.7 170.7 163.6 163.6	1000.0 18.3 17.5 30.0 29.2 39.1 38.1

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ge Parameters (NC	OT cascaded)	Gain [dB]		Output P1dB Compression [dBm]	Output TOI [dBm]
Cmp_Index	Cmp_RefDes	Cmp_S21_dB	Cmp_NF_dB	Cmp_OutP1dB_dBm	Cmp_OutTOI_dBm
0 1 2 3 4 5 6	PAD1 B_HMC451LP3 PAD2 C_HMC773LC3B_2 PAD3 D_TGA2760_SM PAD4	-1.0 18.0 -1.0 -9.0 -1.0 33.0 -1.0	1.0 7.0 9.0 1.0 6.0 1.0	1000.0 19.5 1000.0 * -2.6 1000.0 32.0 1000.0	* 1000.0 * 30.1 1000.0 8.0 1000.0 52.0 1000.0

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* note: some of these numbers are not too accurate...this is a Budget Simulator limitation in the assumed relationship between P1dB compression and TOI

Stage Parameters, Sr		, to otage [ab]	of Stage [dB]	NF, moving forward in chain [dB]	image noise filtering	[dB] in chain [d	•	
Cmp_Index	Cmp_RefDes	InPGain_SS_dB	OutVGainInc_SS_dB	efln_Nolmage_dB	NF_Refln_0	BfOut_N	olmage_dB InTC	DI_dBm
0 1 2 3 4 5 6	PAD1 B_HMC451LP3 PAD2 C_HMC773LC3B_2 PAD3 D_TGA2760_SM PAD4	0.0 -1.0 17.0 16.0 7.0 6.0 39.0	-1.0 17.0 16.0 6.0 39.0 38.0	1.1 8.1 8.1 8.1 8.1 8.1 8.1 8.1		1.0 8.0 8.0 11.1 11.4 11.4 11.4	8.6 7.6 17.0 16.0 7.0 6.0 1.0	0.5 -0.5 17.7 16.7 20.0 19.0 1000.0
			total cascaded s gain		l cascaded NF h mixer image fi	tering) total (with	total I cascaded NF out mixer image filte	Input TOI [dBm] ring)
Single Tone Large Sig	nal Analysis		Power at Output of Stage [dBm]	Freq at Input To Stage [GHz]	Freq at Outpu Of Stage [G⊦			
Cmp_Index	Cmp_RefDes	InPwr_dBm	OutPwr_dBm	InFreq/1e9	OutFreq/1e9	InPGain	_dB OutPGair	n_dB
0 1 2 3 4 5 6	PAD1 B_HMC451LP3 PAD2 C_HMC773LC3B_2 PAD3 D_TGA2760_SM PAD4	-0.0 -1.0 16.6 15.6 -0.3 -1.3 31.1	-1.0 16.6 15.6 -0.3 -1.3 31.1 30.1	5.0 5.0 5.0 11.0 11.0 11.0	1 1 1	.0 .0 .0 .0 .0 .0	0.0 -1.0 16.6 15.6 -0.3 -1.3 31.1	-1.0 16.6 15.6 -0.3 -1.3 31.1 30.1
Noise Power, SNR, SI	EDR Cmp RefDes	InNPwrTotal dB	m OutNPwrTotal	dBm InSNR() dB (utSNR0 dB	OutSFDR ResBW dl	B OutSFDR Total dE
0 1 2 3 4 5 6	PAD B_HMC451LP3 PAD C_HMC773LC3B 2 PAD D_TGA2760_SM PAD		73.9 73.9 49.0 50.0 55.8 56.8 23.6	-173.9 -149.0 -150.0 -155.8 -156.8 -123.6 -124.6	173.9 172.9 165.5 165.5 155.5 155.5 154.7	172.9 165.5 165.5 155.5 155.5 154.7 154.7	1000. 119 119 109. 109. 109. 108. 108.	0 1000 4 119 4 119 0 100 0 100 7 108